



IFW

PATENT  
0649-0941P

## IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: Kenkichi HAYASHI Conf.: 1757  
Appl. No.: 10/770,501 Group:  
Filed: February 4, 2004 Examiner:  
For: A SIGNAL PROCESSING METHOD, A SIGNAL  
PROCESSOR CIRCUIT, AND IMAGING APPARATUS

LETTER SUBMITTING FORMAL DRAWINGS

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

June 8, 2004

Sir:

Attached hereto is/are five (5) sheet(s) of formal drawings which comply with the provisions of 37 C.F.R. § 1.84. The drawings should be made a part of the record of the above-identified application.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. §§1.16 or 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH &amp; BIRCH, LLP

By \_\_\_\_\_  
D. Richard Anderson, #40,439

DRA/lab  
0649-0941P

P.O. Box 747  
Falls Church, VA 22040-0747  
(703) 205-8000

Attachment(s)

(Rev. 02/13/2004)